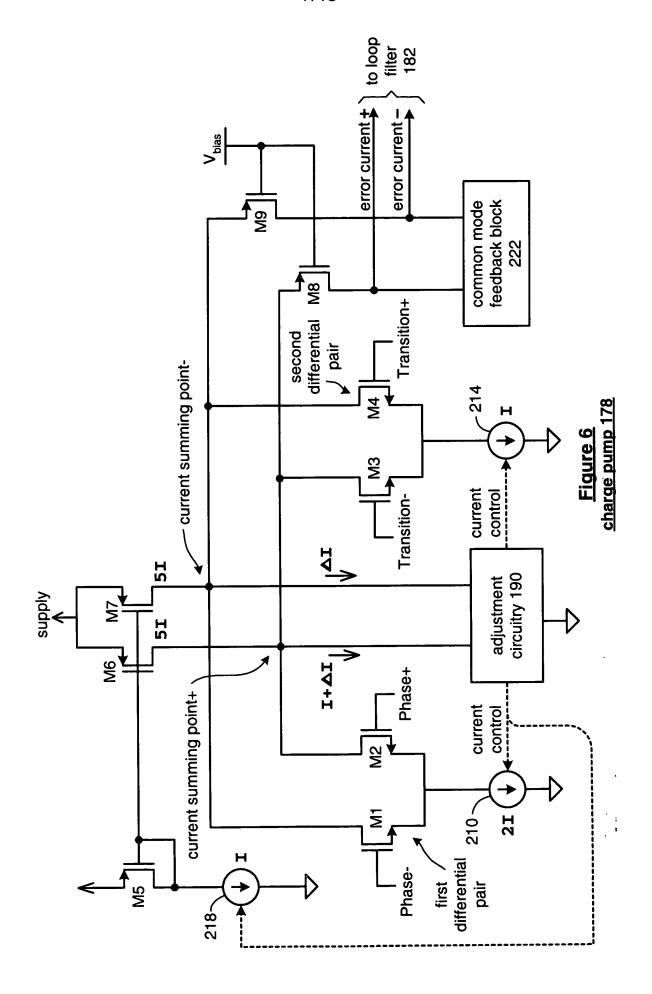
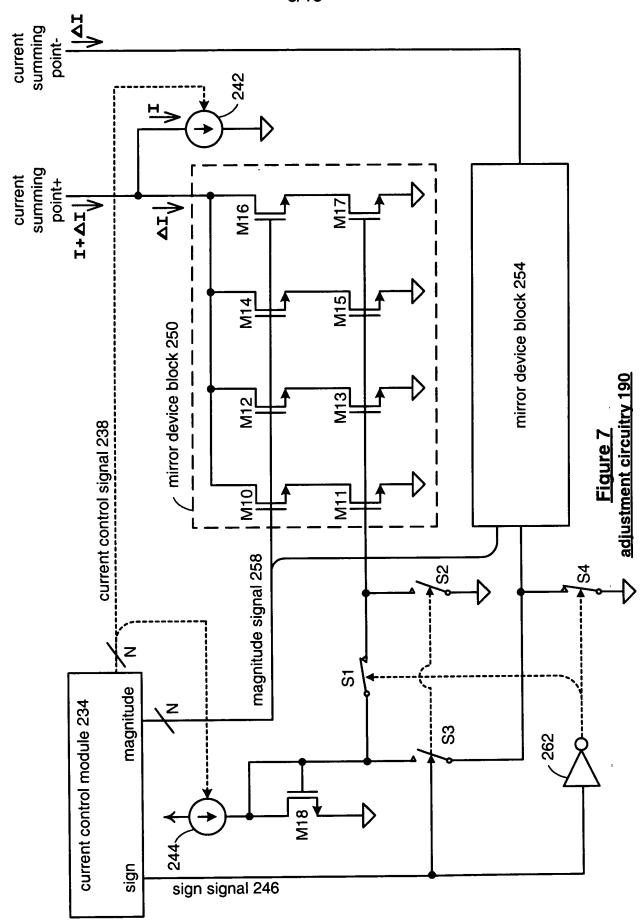
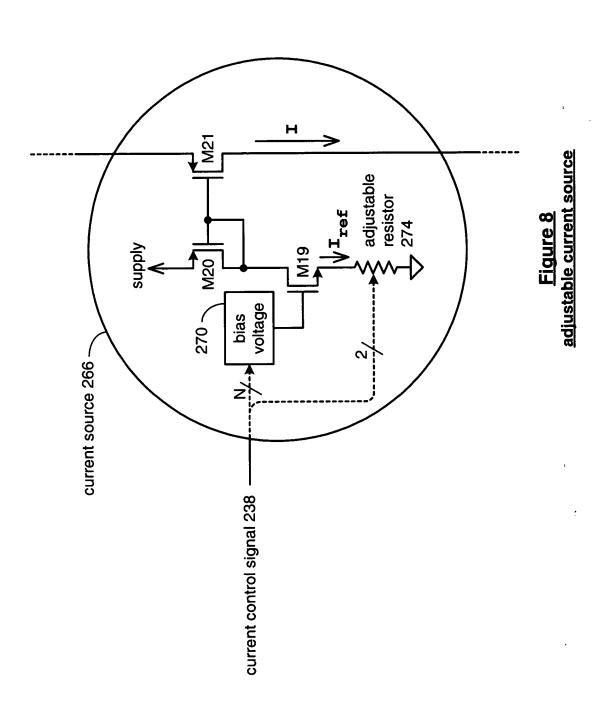


Figure 5 phase-locked loop 170







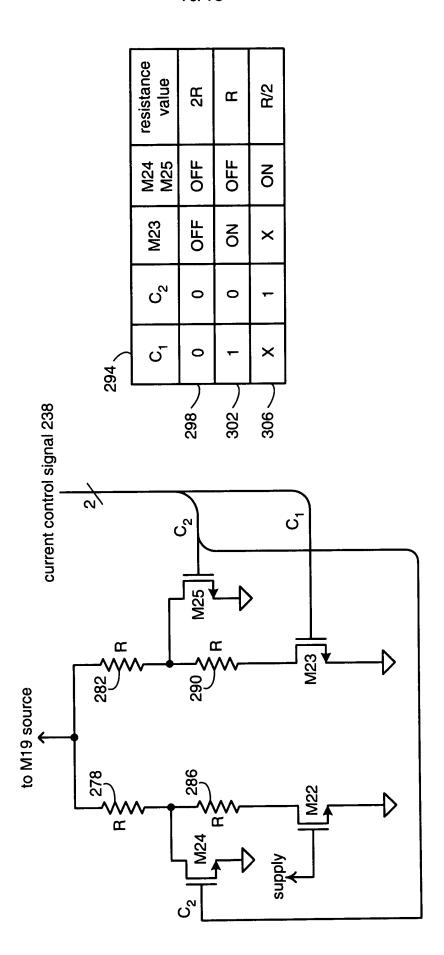
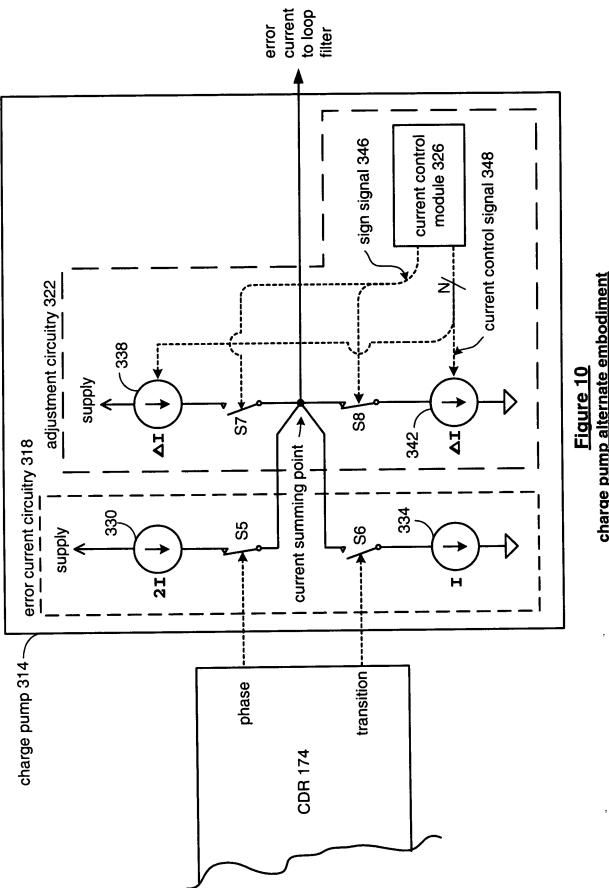


Figure 9 adjustable resistor 274



charge pump alternate embodiment

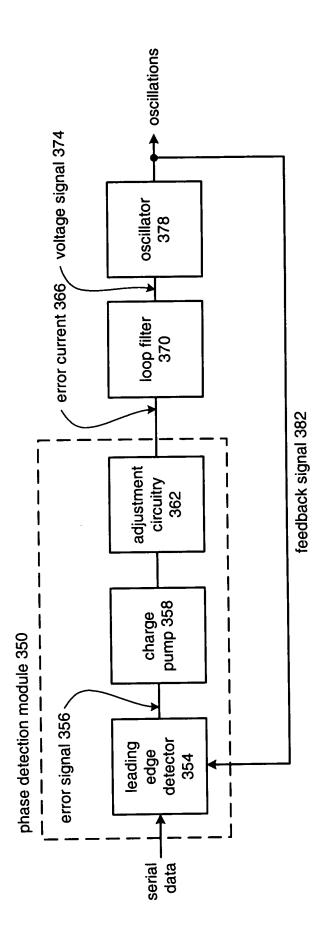
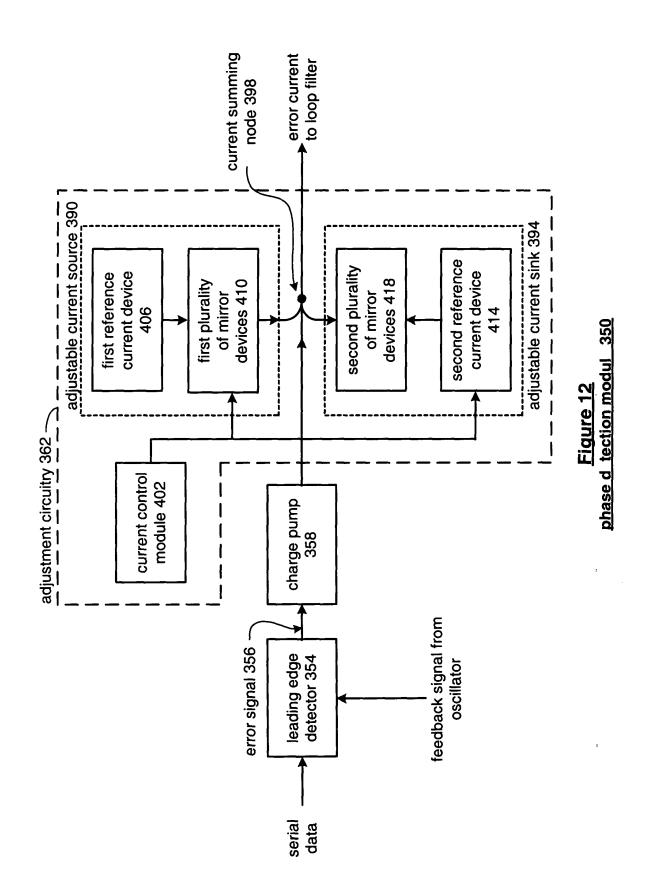


Figure 11 phase detection module



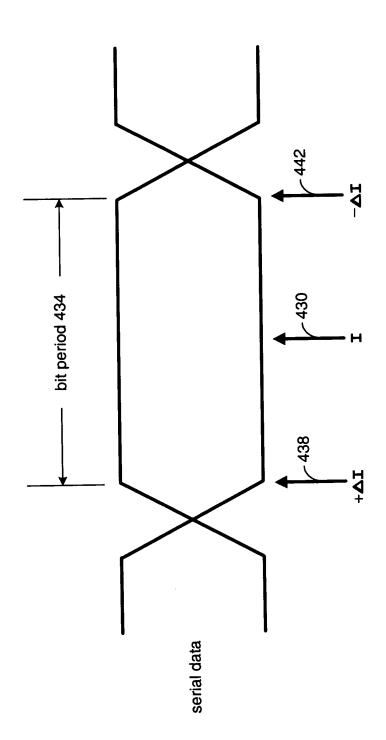


Figure 13 serial data y diagram

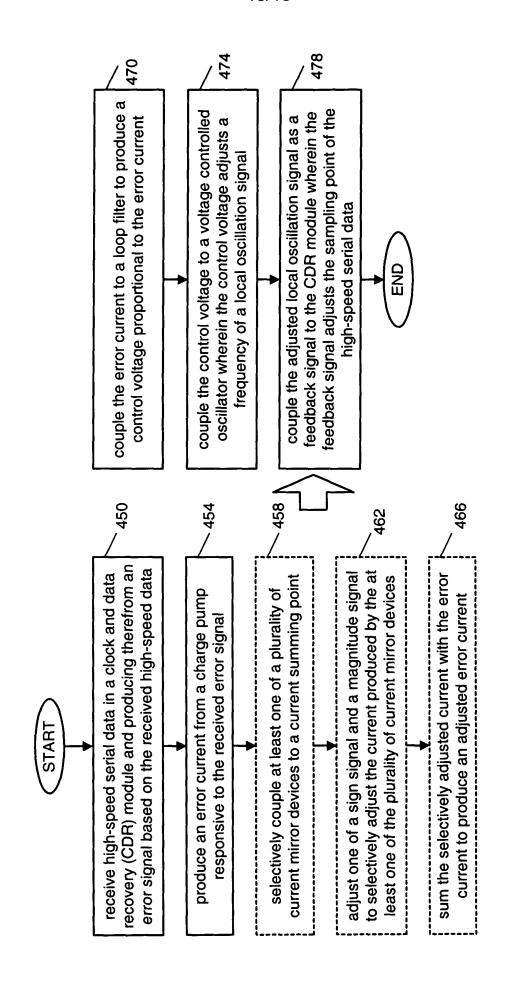


Figure 14
PLL sampling point adjustment method